# MF PAL Clock Test Specification

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Native Test:

Level 0:

* Initialize two gpio pins in output mode (let’s call them pin0 and pin1)
* Create 2 64 bit uint called prev, curr, initialize them to zero
* Inside a while loop
  + Sample the 64-bit clock to curr
  + Check curr > prev
    - True toggle pin0
    - False send a ‘pulse’ pin 1
  + Move curr to prev
* Run Test for 10 mins
* Capture output on logic
* Plot ‘time delay’ between pulses on pin1
* Find the ‘mean’ and ‘variation’ of the time delay on pin1
* If no pulses are captured on pin1 in 10 mins, run longer